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Sheet 1 of 1

Application No. :

Filing Date :

First Named Inventor: M. SHIMOOKA

Group Art Unit :

Examiner Name :

Attorney Docket No. : NEC 20.689

**U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No. <sup>1</sup>	U.S. Patent Document	Kind Code if known <sup>2</sup>	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear

**FOREIGN DOCUMENTS**

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**Other Prior Art-Non Patent Literature Documents**

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CN		KUO-HSING CHENG, et al. The Improvement of Conditional Sum Adder for Low Power Applications/ 1998 IEEE pgs 131-134	
Examiner Signature	/Chuong Ngo/		Date Considered 09/29/2006

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